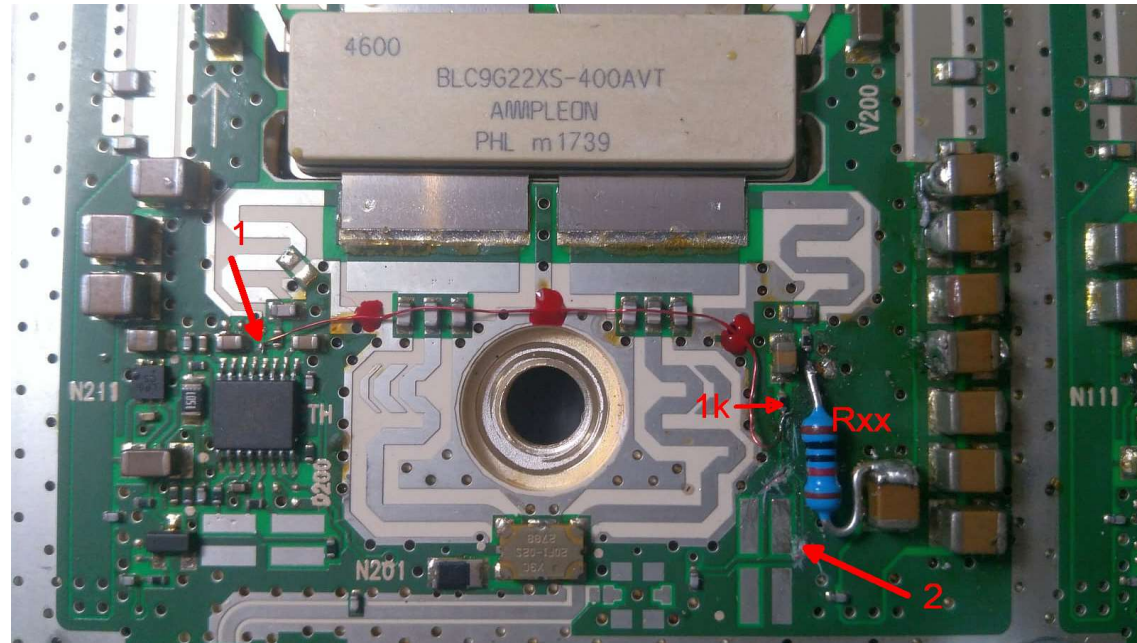
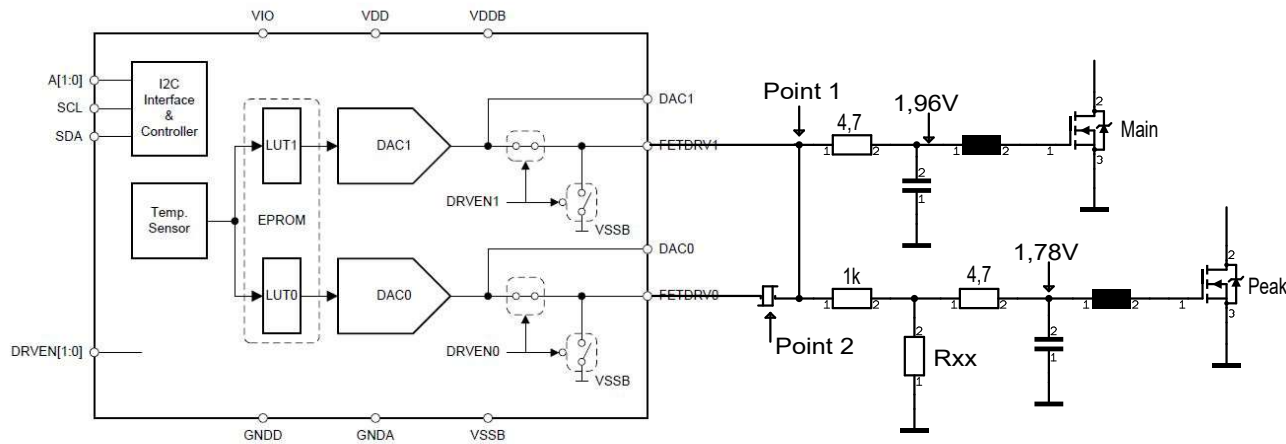


Solder the jumper wire to Point 1  
(Pin 13 FETDRV1 of IC LMP92066).  
and the other end to the SMD-R with 1k



At Point 2: cut the line from SMD-R 1k  
to pin 11 (FETDTV0) of IC LMP92066.

Schematic IC LMP92066



Set the quiescent drain current for Peak LDMOS  
with additional Rxx (10k...15k) to approx. 0.8A

The quiescent drain current of the peak LDMOS  
should roughly match that of the main LDMOS

The measured gate voltages are only  
to be understood as information

Maßstab	100,00%	Firma	Zeichner	DJ7TH	Blatt	1 of 1
Änderung	20.07.23	15:33	Titel			Nokia Dual-PA Modification
Ausgabe	20.07.23	15:35	Peak Gate Supply			
Datei	2.4GHz NokiaPA Interface V2.0.T3001		Projekt			2.4GHZ-Nokia-PA